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Docket No.: M1071.1451/P1451
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kazuya Sayanagi, et al.

Application No.: 09/974,668

Art Unit: 2812

Filed: October 10, 2001

Examiner: Stephen E. Jones

For: HIGH-FREQUENCY CIRCUIT BOARD
UNIT, HIGH-FREQUENCY MODULE
USING THE SAME UNIT, ELECTRONIC
APPARATUS USING THE SAME
MODULE, AND MANUFACTURING
METHOD FOR THE HIGH-FREQUENCY
CIRCUIT BOARD UNIT

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

MS Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Final Office Action dated June 26, 2003, please amend the
above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on
page 3 of this paper.

Remarks/Arguments begin on page 6 of this paper.

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FEE CALCULATION

Any additional fee required has been calculated as follows:

	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	Additional Fee
Total	13	- 20* =	0	X	0.00
Independent	2	- 3** =	0	X	0.00
First presentation of Multiple Dependent Claim(s) (if applicable)					
TOTAL					0.00

*not less than 20

** not less than 3

In the event a fee is required or if any additional fee during the prosecution of this application is not paid, the Patent Office is authorized to charge the underpayment to Deposit Account No. 50-2215.

CONTINGENT EXTENSION REQUEST

If this communication is filed after the shortened statutory time period had elapsed and no separate Petition is enclosed, the Commissioner of Patents and Trademarks is petitioned, under 37 CFR 1.136(a), to extend the time for filing a response to the outstanding Office Action by the number of months which will avoid abandonment under 37 CFR 1.135. The fee under 37 CFR 1.17 should be charged to our Deposit Account No. 50-2215.

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A high-frequency circuit board unit comprising:
a circuit board including a ground electrode and a terminal electrode; and
a semiconductor device mounted on said circuit board, said semiconductor device
including a high-frequency signal terminal for sending and receiving a high-frequency signal
to and from said terminal electrode of said circuit board and a non-high-frequency signal
terminal through which said high-frequency signal is not sent or received,

wherein at least one of said terminal electrode of said circuit board and said high-frequency signal terminal of said semiconductor device is connected to said ground electrode of said circuit board for conducting direct current.

2. (Original) A high-frequency circuit board unit according to claim 1, further comprising a passive impedance circuit device mounted on said circuit board and connected between said high-frequency signal terminal and said terminal electrode,

wherein one of said high-frequency signal terminal and said terminal electrode is connected to said ground electrode for conducting direct current via said passive impedance circuit device.

3. (Original) A high-frequency circuit board unit according to claim 1, further comprising a passive impedance circuit device mounted on said circuit board and connected between said high-frequency signal terminal and said terminal electrode,

wherein both said high-frequency signal terminal and said terminal electrode are connected to said ground electrode for conducting direct current via said passive impedance circuit device.